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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re the Application of

Attn: OIPE

Sadayoshi HORII et al.

Application No.: 10/529,466

Docket No.: 123373

Filed: May 2, 2005

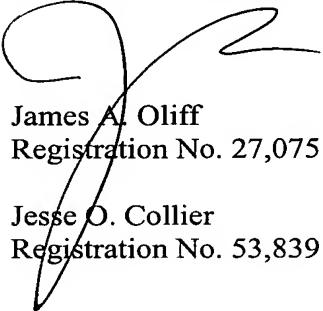
For: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND
SUBSTRATE PROCESSING APPARATUS**REQUEST FOR CORRECTION OF PALM RECORDS**

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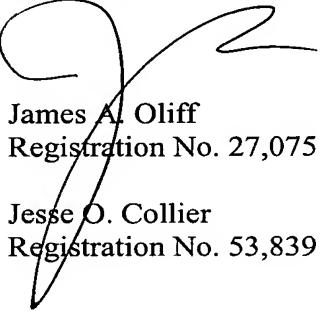
Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,


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Date: October 17, 2005

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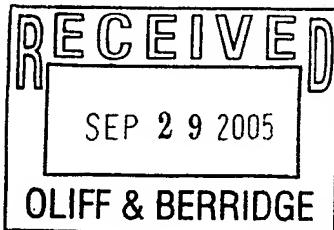


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/529,466	09/29/2005	2812	900	123373	11	11	2

5/2/2005

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CONFIRMATION NO. 8082
FILING RECEIPT



OC000000017086170

Date Mailed: 09/28/2005

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Assignment For Published Patent Application

Hitachi Kokusai Electronic, Inc., Nakano-ku, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/13606 10/24/2003

Foreign Applications

JAPAN 2002-316154 10/30/2002

Projected Publication Date: 12/29/2005

Non-Publication Request: No

Early Publication Request: No

Title

~~Method for manufacturing semiconductor device and substrate processing system~~

**MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND SUBSTRATE
PRELIMINARY CLASS PROCESSING APPARATUS
438**

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